

DS-C60S-DEC **Decoding board**



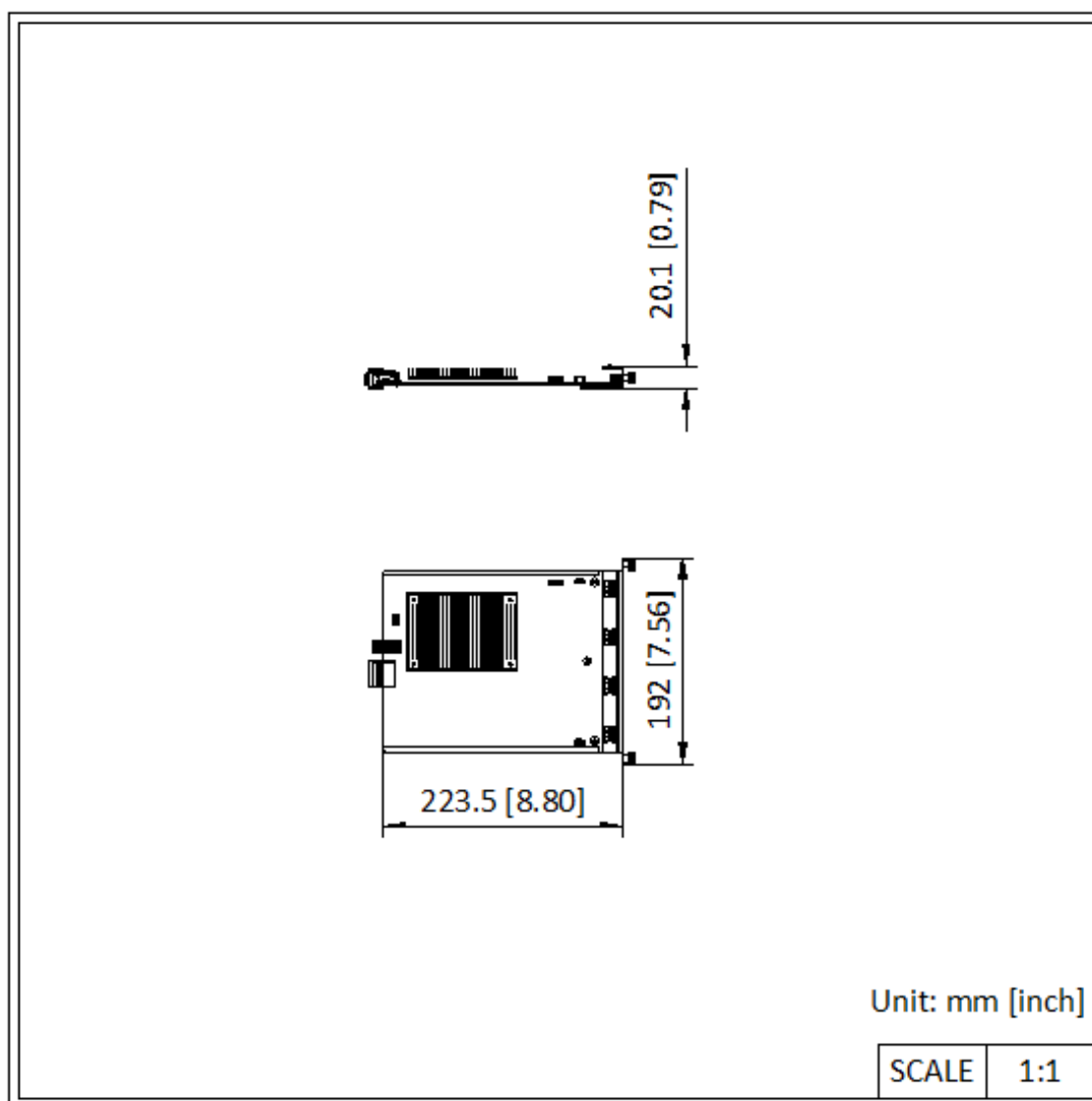
The video wall controller is mainly used for screen splicing control system, and is the core control device of the system. As a new-generation FPGA-based pure hardware image processing device, it adopts the structure of main control board and service boards. It supports the video input and video output via various ports. It supports the network encoding and real-time preview of signal sources. It supports the decoding and output of various network signal sources. It supports the high-definition (HD) video splicing and fusion. It supports the window splicing, roaming window, and other operations. It supports the management on users, network, operation, alarm and logs.

- Supports a maximum of 2 decoding boards.
- Supports decoding a maximum of 1 channel of 32 MP@30 fps data or 2 channels of 16 MP@30 fps data.
- Supports de-multiplexing of PS, TS, ES, and RTP streams.
- Supports H.265, H.264, Smart H.265, and Smart H.264 video decoding.
- Supports G711-A, G711-U, G722.1, G726-16/U, MPEG, and AAC-LC audio decoding.

▪ Specification

Chassis	
Occupied Slot	Input slot
Video Decoding	
Video Decoding Format	H.264, H.265, Smart264, Smart265, MJPEG
Video Decoding Channels	48
Video Decoding Capability	Smart H.265/Smart H.264/H.264/H.265: 24 channels of 1080p@30 fps MJPEG: 4 channels of 1080p@30 fps Supports decoding a maximum of 1 channel of 32 MP@30 fps data or 2 channels of 16 MP@30 fps data.
Video Decoding Resolution	≤ 32 MP
Audio Decoding	
Audio Decoding Format	G711-A, G711-U, G722.1, G726-16/U, MPEG, AAC-LC

▪ Dimension



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